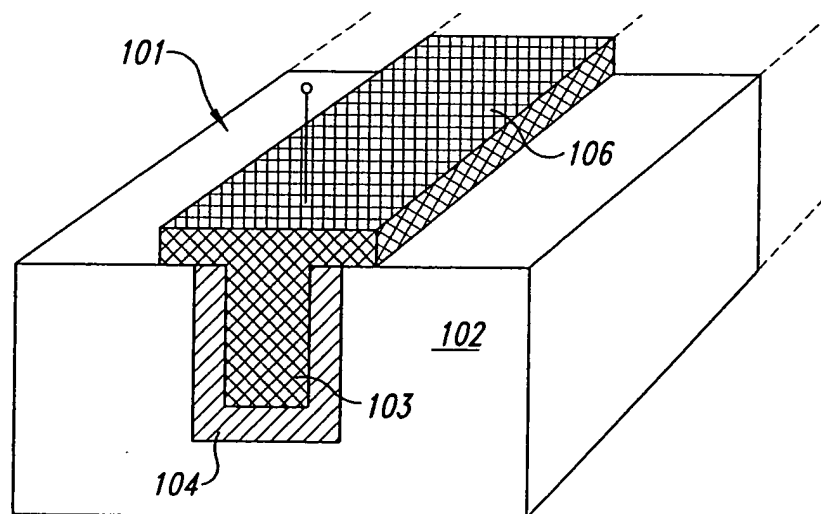


*Fig. 9*



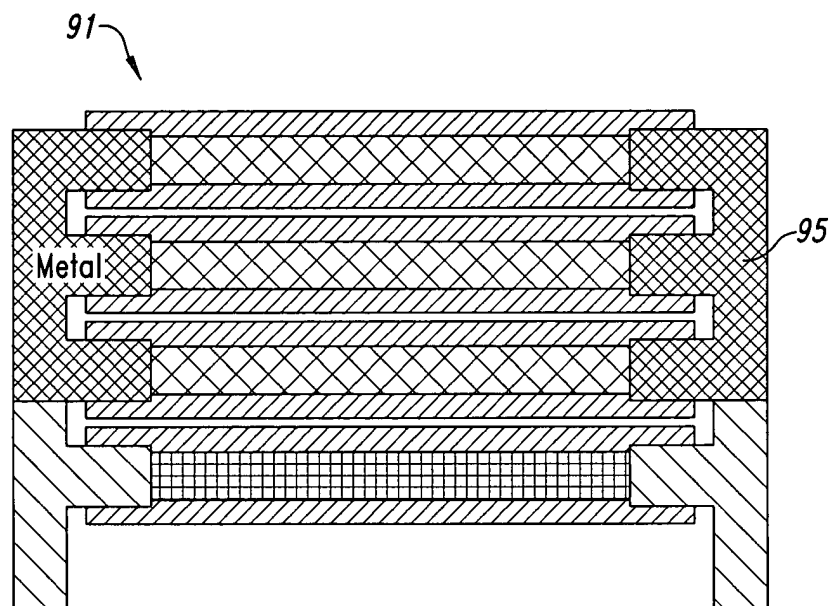
*Fig. 10*



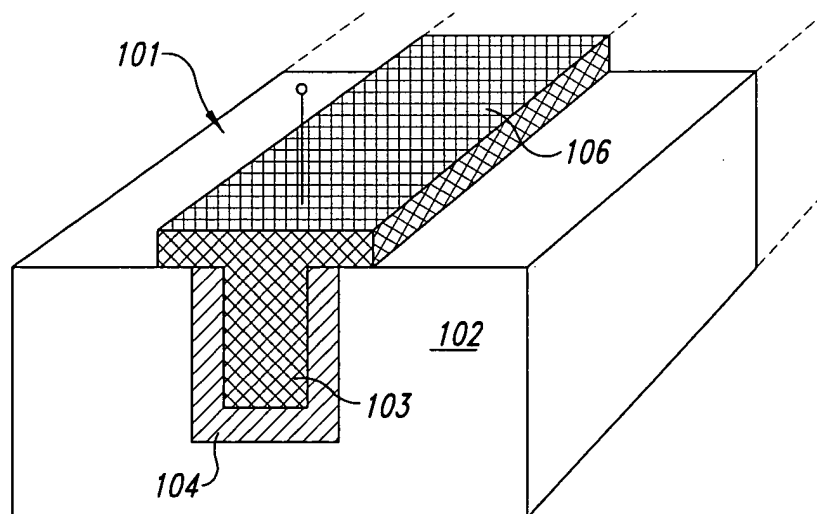
Title: RESISTIVE STRUCTURE INTEGRATED IN A SEMICONDUCTOR SUBSTRATE

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Inventor(s): Salvatore Leonardi et al. Serial No. 09/747,425 Docket No. 856063.672



*Fig. 9*



*Fig. 10*